

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Cho et al.

Serial No.: To Be Assigned

Filed: Concurrently Herewith

For: INTEGRATED CIRCUIT DEVICES HAVING DIELECTRIC REGIONS
PROTECTED WITH MULTI-LAYER INSULATION STRUCTURES AND
METHODS OF FABRICATING SAME

Date: March 30, 2004

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Sir:

Prior to the examination of the above-referenced application, please enter the following amendments below. Applicants provide the present Preliminary Amendment pursuant to the rules stated in revised 37 C.F.R. 1.121 that became effective on July 30, 2003.